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		FILING DATE June 4, 2001	GROUP 2815

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
EO	AA	5,767,553	06/16/98	Lo et al.			
EO	AB	5,208,132	05/04/93	Narayan et al.			
EO	AC	5,877,074	03/02/99	Jeng et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AM		Wolf et al., "Silicon Processing for the VLSI Era - Volume 1: Process Technology," ©1986 Lattice Press, pages 384-385 (4) pages total)
	AN		
	AO		

U.S. Patent Office *8/5/03*

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.